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S7	95	("4875451" "4922418" "5007732" "5277192" "5390286" "5515327" "5712996" "6119721" "6141313" "4315315" "4400846" "4609872" "4764925" "4800889" "4847755" "4855903" "5021947" "5241635" "5277196" "5301141" "5369775" "5388189" "5448598" "5454104" "5457640" "5507412" "5524629" "5562101" "5603012" "5616840" "5703793" "5727233" "5734586" "5740460" "5742814" "5765037" "5768561" "5768629" "5784631" "5798719" "5809270" "5829007" "5842033" "5857183" "5870588" "5872949" "5881301" "5929858" "5938748" "5952569").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 11:10

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S12	4	("5963972").URPN.	USPAT	OR	OFF	2005/03/16 12:45
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S14	122	graph with node$1 with dependenc$3 with edg$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:11
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S17	0	S16 and (execut$3 with node$1 with (color$1 or visual$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:21
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S20	1	S16 and (color$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:19
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S22	0	S16 and (display$3 with execut$3 with node$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:30
S23	0	S16 and (display$3 with unexecut$3 with node$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:30
S24	618	(display$3 with execut$3 with node$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 13:30
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S27	307	717/151.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 16:25
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S30	62	717/157.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 16:26
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September 1995 **Proceedings of the 22nd annual conference on Computer graphics and interactive techniques**
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Douglas N. Gordin
June 2002 **Proceedings of the 2nd international symposium on Smart graphics**

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Graphs offer a powerful way to view the relationships between objects. Yet, as useful as small graphs are for seeing relationships, large graphs are frustrating because their complexity overwhelms our ability to trace through patterns of relationships. The *Raisin* system helps manage this complexity by giving the means to layout a graph well; index a graph using categories based on structure and other criteria; highlight and abstract via selection, hiding, and aggregation; and create new c ...

Keywords: aggregation, categorization, directed acyclic graph, focus+context, graph visualization, simplification, tree control

2 New frontiers in logic synthesis: Multiple constant multiplication by time-multiplexed mapping of addition chains
Peter Tummeltshammer, James C. Hoe, Markus Püschel
June 2004 **Proceedings of the 41st annual conference on Design automation**

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An important primitive in the hardware implementations of linear DSP transforms is a circuit that can multiply an input value by one of several different preset constants. We propose a novel implementation of this circuit based on combining the *addition chains* of the constituent constants. We present an algorithm to automatically generate such a circuit for a given set of constants. The quality of the resulting circuits is evaluated after synthesis for a commercial 0.18um standard cell li ...

Keywords: addition chains, directed acyclic graph, fusion, multiplierless

3 Task scheduling using a block dependency DAG for block-oriented sparse Cholesky factorization
Heejo Lee, Jong Kim, Sung Je Hong, Sunggu Lee
March 2000 **Proceedings of the 2000 ACM symposium on Applied computing**

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Keywords: block-oriented Cholesky factorization, directed acyclic graph, parallel sparse

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J. L. Baer
January 1973 **ACM Computing Surveys (CSUR)**, Volume 5 Issue 1

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Siddhartha Chatterjee
July 1993   **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 15
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        Keywords: compilers, data parallelism, shared-memory multiprocessors

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January 1980 **ACM SIGACT News**, Volume 12 Issue 1

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4 Tutorial: Compiling concurrent languages for sequential processors   ▓
Stephen A. Edwards
April 2003   **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 8
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        Embedded systems often include a traditional processor capable of executing sequential code, but
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        Keywords: Compilation, Esterel, Lustre, Petri nets, Verilog, code generation, communication,
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